Fault-based Testing of E-Commerce Applications

Marisa A. Sánchez¹, Juan Carlos Augusto², and Miguel Felder³

¹ Dpto. de Cs. de la Administración, Universidad Nacional del Sur, Argentina

² School of Computing and Mathematics, University of Ulster at Jordanstown, UK

³ Pragma Consultores, Buenos Aires, Argentina

Abstract. Because of their complexity, business transactions are prone to failure in many ways. This paper reports on our experience using a fault-based testing approach. The approach overcomes the limitations of specification-based approaches that derive from the intrinsic incompleteness of the specification, and from the focus of specifications on correct behaviors, rather than potential faults, and hence provides a complementary technique during the testing phase.

1 Introduction

Because of their complexity, business transactions are prone to failure in many ways. For example, a request that is satisfied under normal conditions can be unexpectedly rejected. That can be experienced in daily life when a Web server is not available because it is busy; when we cannot properly fill in an order form because we are not able to view information on a low resolution screen; or when we abandon a page because it makes heavy use of cookies and we have turned on alerts every time a cookie is activated. However, systems are normally built considering the normal and expected pattern of behavior. Thus, a fault-based testing approach that considers the way the system behavior can be compromised by failures or abnormal conditions or interactions is desirable. Testing is faultbased when its motivation is to demonstrate the absence of pre-specified faults.

Although there are many proposals of fault-based testing [1,2], all are concerned with syntactic errors (e.g., errors in the use of relational or arithmetic operators, incorrect variable references, etcetera). These types of errors represent only a small portion of possible errors. In real applications, faults depend on a multiplicity of causal factors including technical, human and institutional aspects. This fact motivates our interest to perform a fault-based testing that is not restricted to syntactic errors.

The work presented in [3] extends the scope of traditional fault-based approaches to semantic errors. This approach propose to characterize possible behaviors and rank them according to some criteria. Fault Tree Analysis is used to determine how an undesirable state (failure state) can occur in the system [4]. Then Fault Tree Analysis results are integrated with the specification of the

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Fig. 1. Statechart for class Order

desired behavior for the system using statecharts [5]. As a result, we obtain a testing model that provides a representation of the way the system behavior can be compromised by failures or abnormal conditions or interactions. The integration is possible since the results of the analysis are expressed in terms of Duration Calculus formulas and we can apply some conversion rules of a formula to a statechart. As a result, we obtain a testing model that provides a representation of the way the system behavior can be compromised by failures or abnormal conditions. In this way, we can automatically derive fault based test cases from the model.

The remainder of the paper is organized as follows. In Sec. 2 a case study that we used as the basis for our work is introduced. Then in Sec. 3 we briefly introduce Fault Tree Analysis. Sec. 4 is devoted to illustrate the fault-based testing approach, and the final conclusions are provided in Sec. 5.

2 Case Study

As an example of an e-commerce application consider OfficeWeb that sells office equipment to larger companies. For space reasons, we only include the views of the model necessary for our purpose. Order is one of the key concepts in the organization, and the behavior of the order is modeled in Figure 1 using a statechart diagram. An order has five states: ReadingForm, AuthorizingCredit-CardSale, AuthorizingInvoiceSale, Placed, and Rejected. If an invoice sale is not authorized because the customer has not enough credit, then he is asked to pay using a credit card.

Additionally, it is interesting to investigate if this normal pattern of behavior specified for the Order, can be corrupted by unexpected conditions. For example, it is typical that when a Web application is subject to unusual levels of activity, it may be unavailable for some time; and the applications that depend on it do not properly handle the error. In particular, consider the scenario in which an order is being processed and the Credit Card Verification System is not available. It is necessary to test how the sales system behaves under this condition.

3 Fault Tree Analysis

Fault Tree Analysis (FTA) is a widely used technique in industrial developments, and allows to describe how individual component failures or subsystems can



Fig. 2. Fault Tree for Order Rejected

combine to effect the system behavior. The construction of a fault tree provides a systematic method for analyzing and documenting the potential causes of a system failure. The analyst begins with the failure scenario of interest and decomposes the failure symptom into its possible causes. Each possible cause is then further refined until the basic causes of the failure are understood. A fault tree consists of the undesired top state linked to more basic events by logic gates. Here we only consider and, or gates, as fault trees containing other gates may be expressed in terms of these. In general, fault trees do not use the not gate, because the inclusion of inversion may lead to non-coherent fault trees, which complicates analysis [6]. Once the tree is constructed, it can be written as a Boolean expression and simplified to show the specific combinations of identified basic events sufficient to cause the undesired top state. The sets of basic events that will cause the root event are regarded as Minimal Cut Sets. The Minimal Cut Set representation of a tree corresponds to an or gate with all the minimal cut sets as descendants.

As an illustration of the use of FTA, consider OfficeWeb order system and a fault tree for the hazard Order Rejected in Figure 2. For reasons of space, we do not include a full description of nodes C8, C9 and C10. There are 9 cut sets: $\{C8\}, \{C9\}, \{C10\}, \{C1\}, \{C2\}, \{C3,C4\}, \{C3,C5\}, \{C3,C6\} and \{C3,C7\}.$

3.1 Fault tree semantics

In [7] fault trees have been given a formal semantics based on a real-time interval logic, the Duration Calculus (DC) [8]. In DC a system is modeled by a number of functions from a temporal structure isomorphic to \mathbf{R}^+ to a set of Boolean

values. These functions are called the state variables of the system. For a state variable (or a Boolean combination of state variables) P, its duration in a time interval, written $\int P$ in DC, is the integral of P over the time interval.

The semantics of a fault tree is determined by the semantics of the leaves, the edges, and the gates, such that the semantics of intermediate nodes are given by the semantics of the leaves, edges, and gates in the subtrees in which the intermediate nodes are roots. A leaf node is interpreted as a formula that may be the occurrence of a state P, i.e. $\lceil P \rceil$; the occurrence of a transition to state P, i.e. $\lceil \neg P \rceil$; $\lceil P \rceil$; a threshold of some duration, i.e. $\int P \leq 8$. For example, in Figure 2, the basic event C8 ("Incorrect calculation of credit limit") may be denoted by the formula $\lceil \text{CreditLimitCalc} \rceil$; $\lceil \text{FailureCreditLimitCalc} \rceil$. CreditLimitCalc denotes the software action to calculate the credit limit, and FailureCreditLimitCalc represents the software failure to accomplish this. The semantics of intermediate nodes depends on the structure of the subtree. For example, the semantics of an intermediate node connected to nodes $B_1, ..., B_n$ through an and gate is given by the conjunction of the semantics of $B_1, ..., B_n$.

4 Construction of the Testing Model

This section is devoted to briefly describe the overall testing framework. Given a fault tree, we should calculate the set of basic events (cut sets) that can conduct to the failure state. For each cut set we construct a statechart based testing model. Using this model, we can automatically derive test cases (see Sec. 4.3).

4.1 Duration Calculus Formulas that represent a cut set

A fault tree describes the events that contribute to an undesirable system behavior, and also what components participate and which responsibilities do they have. In Sec. 3.1, we described how fault trees are interpreted as temporal formulas. Thus, we can interpret each cut set as a DC formula. For example, for the cut set $\{C3, C4\}$ ("Request for credit card verification" and "Unavailability of credit card verification system") we provide the following DC formula:

$c_1 \stackrel{\text{def}}{=} [\texttt{RequestCCV}]; [\texttt{CCVSUnavailable}]; [\texttt{OrderRejected}]$

The formula denotes (a) the occurrence of a request for credit card verification (RequestCCV), (b) the unavailability of the credit card verification system (CCVSUnavailable), and (c) the rejection of the order (OrderRejected).

Given a cut set that contains n basic events $CS_i = \{e_1, e_2, ..., e_n\}$, the formula that describes it has the form: $cs_i \stackrel{\text{def}}{=} f_1 \wedge f_2 \wedge ... \wedge f_n$, where each f_i represents the basic event e_i , for $1 \leq i \leq n$, and it refers to a single component. That is, we should include each f_i in the specification statechart of the appropriate component. For reasons of space, we consider a small example, and thus we cannot exploit the expressive potential of DC. However, note that the formal semantics for the graphic representation of fault trees makes statements in the leaves precise. This precision is necessary to perform an adequate integration of these results with the specification statecharts.



Fig. 3. Slice based on cut set $\{C3, C4\}$

4.2 Integration

In [3], we presented the conversion rules of a formula to a statechart. The rules are applied to the syntactic categories of Duration Calculus formulas. As an example, consider the cut set $\{C3, C4\}$, which is described with the formula [RequestCCV]; [CCVSUnavailable]; [OrderRejected]. The components RequestCCV and OrderRejected refer to the statechart's states AuthorizingCreditCardSale and Rejected, respectively. RequestCCV is an action taken in state Authorizing-CreditCardSale. Then, the formula denotes a transition from state Authorizing-CreditCardSale to state Rejected, and the transition is triggered by condition CCVSUnavailable (represented by [CCVSUn] in the statechart).

Finally, we obtain the statechart of Figure 3 as a system model with respect to cut set $\{C3, C4\}$. In order to obtain a testing model based on this scenario, we do not need to include the whole product of the proposed statechart. We calculate a slice using the slicing technique described in [9]. The slice includes the shaded states and transitions t_1, t_2, t_8, t_5, t_7 (see Figure 3). Note that in any non trivial model, the benefits of reducing the model become apparent.

4.3 Test sequence definition

In [10] the authors describe how to generate test cases based on Unified Modeling Language (UML) state diagrams [11]. These diagrams are based on Statecharts. We consider their approach to generate test cases for statecharts. Then, using a state coverage criteria we obtain the following test sequences: S_1 : (ReadingForm, t2, AuthorizingCreditCardSale,t8, Rejected) (conditions [Credit Card-Sale], [CCVSUn] should evaluate to true); and S_2 : (ReadingForm, t5, AuthorizingInvoiceSale, t7, AuthorizingCreditCardSale, t8, Rejected) (conditions [CreditSale], [credit < total sale], [CCVSUn] should evaluate to true to exercise the test cases.

5 Conclusions

This work reports on our experience using a fault-based testing approach to a business application. The approach has been previously proposed for another application domain (control systems) [3], and in this work we illustrate its usel-fullness for an e-commerce system.

In business applications, faults depend on a multiplicity of causal factors, and this approach allows to consider combinations of faults. We use Fault Tree Analysis to determine how an undesirable state can occur in the system. The results of this analysis expressed in terms of DC are integrated with the system specified behavior to determine how we can reproduce such behaviors. If the Fault Tree Analysis is complete, then the testing approach assures that all conditions that enable a fault situation will show up as test cases.

Still, our experiments show that most of the scenarios resulting from Fault Tree Analysis would be better integrated with models of the processes rather than with statecharts. In business models, statecharts are used to represent individual resource behavior [12]. Activity (or process) diagrams show the control and data flow of processes, and thus involve the interaction among different resources and products. Interesting fault conditions arise when individual components can combine to affect the system behavior, and this information is closer to a process description. One direction for further work is to describe how to integrate fault tree analysis results with process models.

The combination of Fault Tree Analysis and statecharts, poses a problem, such as the integration of heterogeneous specifications. Most of the tasks involved, i.e. the conversion of Duration Calculus formulas to statecharts, the slicing and the generation of test sequences can be automated. Much of the ongoing work is directed at developing tool support.

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